



(An Institution of National Importance under MHRD, Government of India)

Melakkotaiyur, Off Vandalur-Kelambakkam Road, Chennai-600127

Ph: +91 44 2747 6393 Fax: +91 44 2747 6301

Email: sricce@iiitdm.ac.in

Advt. No: IIITDMK/PR/JRF/A17/2023

Date : 04/07/2023

Advertisement for Junior Research Fellow

Indian Institute of Information Technology Design and Manufacturing (IIITDM), Kanchipuram is an Institution of National Importance established in 2007 by the Ministry of Human Resource Development, Government of India and is located at outskirts of Chennai (Off Vandalur-Kelambakkam Road). It is a Centre of Excellence for pursuing Design and Manufacturing Oriented Engineering Education and Research and for promoting competitive advantage of Indian products in global markets.

IIITDM Kanchipuram is presently offering undergraduate, postgraduate, dual-degree programmes in the areas of Computer, Electronics, Mechanical Engineering and Inter-Disciplinary Ph.D. programmes in core and applied areas of basic sciences (Physics and Mathematics) and engineering. It functions with the vision to groom engineers with design and manufacturing skills. The institute gives significant amount of emphasis for practice courses and theory concepts are also explored along with the relevant laboratory course.

IIITDM Kanchipuram invites **online application** for the following research project position:

Name of the Research Project	FPGA/ASIC/PSoC Development of Identification of stages of sleep in critical neurological illness cases from continuous EEG signal using Neuromorphic circuit
Duration of the project	43 months, up to 10/02/2027
Duration of Appointment	Initially One year and may be extended
Name of the Principal Investigator	Prof. Binsu J Kailath
Funding Agency	Category-III, Chips to Startup (C2S) Programme, MeitY, Govt. of India
Name of the Project Position	Project Associate - II
Qualification	B Tech/B E in Electronics and Communication Engg and M Tech in Microelectronics and VLSI Design / VLSI System Design / Electronic System Design
Skillset required	Expertise in Verilog coding, Familiarized with Vivado/Cadence/Synopsys tools for FPGA/ASIC Design flow
Age	Less than 35 years
Stipend (Consolidated)	Rs. 35000 / p.m.

General Conditions/Instructions for all posts:

1. Candidates need to apply online through institute website only.
2. Submission of application form online is sufficient.
3. Download and print the application form submitted and keep safe.
4. Printed application form needs to be brought to the Institute for attending the test/interview.
5. Only Indian Nationals can apply for the post.
6. The selected candidate is required to meet the objectives and achieve the deliverables of the project.
7. The position is temporary.
8. Shortlisted candidates will be intimated through email.
9. Shortlisted candidates will have to appear for a written test and interview.
10. Candidates should bring self-attested copies of the relevant certificates/testimonials along with the original certificates/documents at the time of interview.
11. No travelling or any other allowances is admissible for attending the test/interview.
12. The Institute reserves the right not to fill up the position, if it so decides.
13. The Institute reserves the right to terminate the appointment at any time before completion of the period, if it so decides.
14. No interim correspondence will be entertained.
15. Canvassing in any form will lead to disqualification.
16. Last date for submission of online application is 30/07/2023

Place of Test/Interview:

Indian Institute of Information Technology Design and Manufacturing, Kancheepuram

Melakkottaiyur, Off Vandalur-Kelambakkam Road,
Chennai-600127

Contact No: 044-27476393/ 9884201895

Email: srcice@iiitdm.ac.in / bkailath@iiitdm.ac.in

Sd/x

Dean (SR)

IIITDM Kancheepuram